



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: M. Nandakumar et al. Examiner: Edgardo Ortiz
Serial No.: 09/876,292 Art Unit: 2815
Filed: 06/07/2001 Docket: TI-31089
For: **ADDITIONAL N-TYPE LDD/POCKET IMPLANT FOR IMPROVING
SHORT-CHANNEL NMOS ESD ROBUSTNESS**

2815
#10
formal
drawgs
4-1803
abf

March 26, 2003

Commissioner for Patents
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Ginger Cox
Ginger Cox

Sir:

SUBSTITUTE DRAWINGS

Further to our facsimile of March 19, 2003, and responsive to the initial telephone call from Examiner Ortiz, enclosed for filing in this case are the original formal drawings for substitution.

Respectfully submitted,

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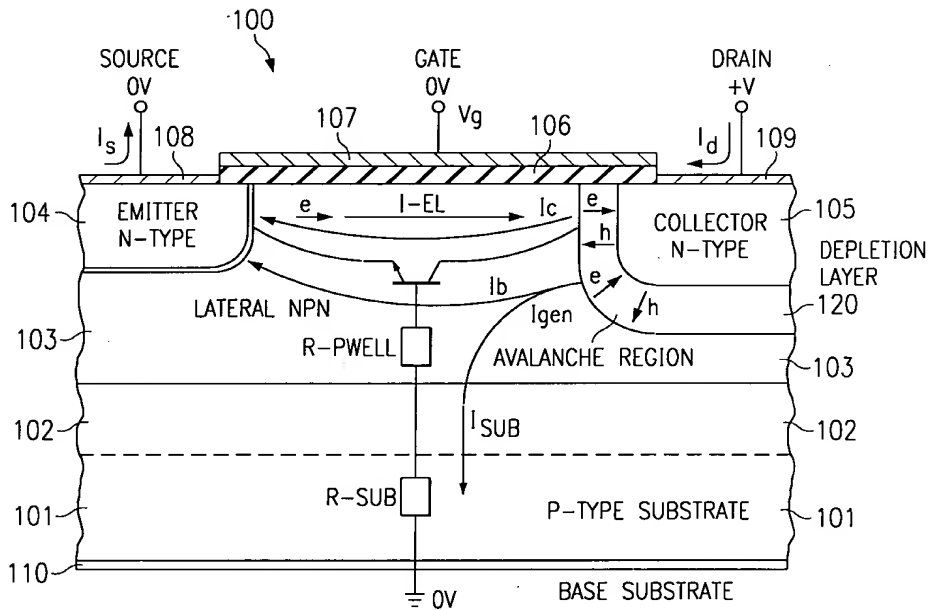


FIG. 1
(PRIOR ART)

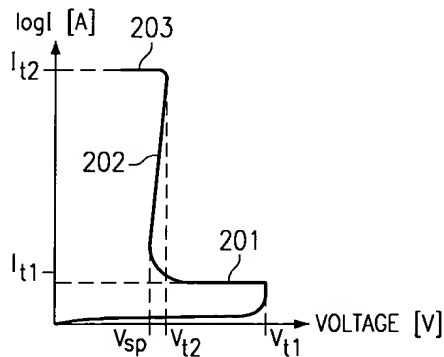


FIG. 2
(PRIOR ART)

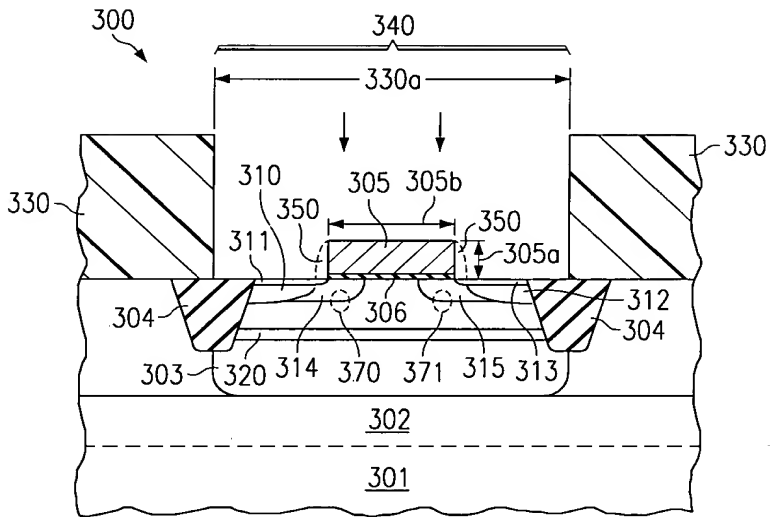


FIG. 3

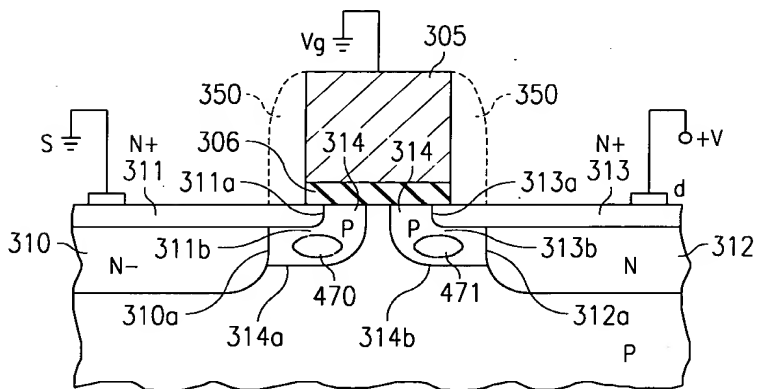


FIG. 4

3/3

FIG. 5

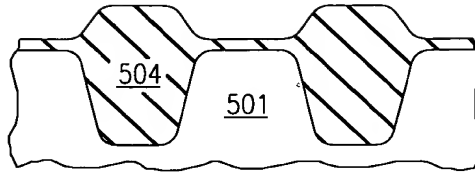


FIG. 6

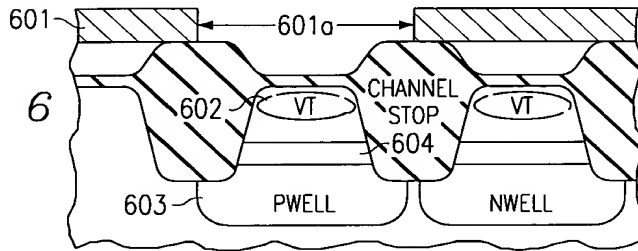


FIG. 7

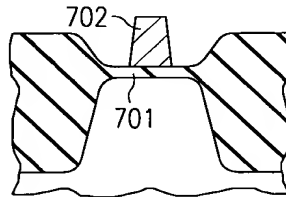


FIG. 8

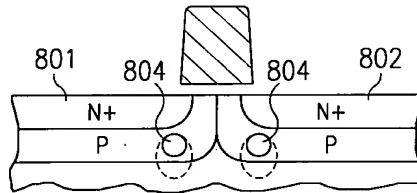


FIG. 9

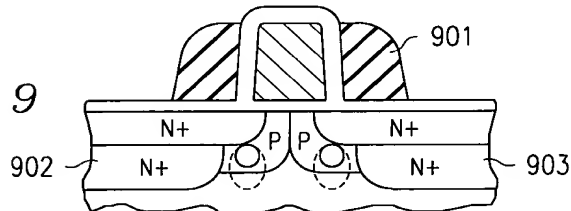


FIG. 10

